

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	120	(DLL or PLL or DPLL or delay\$3 adj2 lock\$3 adj2 loop\$1 or phase adj2 lock\$3 adj2 loop\$1) same (sync\$15 near2 track\$3 or sync\$15 near2 acquisition or sync\$15 near2 circuit\$3 or sync\$15 near2 track\$4 or sync\$15 near3 recover\$4 or track\$3 near3 phase) same (CDMA or despread\$4 or de adj1 spread\$4 or rake near2 receiv\$4 or finger\$1 near2 receiv\$4 or rake near2 finger\$1 or finger\$1 near3 demodulat\$4)	USPA T; US-P GPUB ; EPO; JPO	2004/08/28 20:26	
2	BRS	L18	4	(interference near2 cancel\$8 or interference near2 eliminat\$4 or interference near2 remov\$4 or noise near2 cancel\$8 or noise near3 eliminat\$4 or noise near3 remov\$4 or noise near2 suppress\$4 or interference near2 suppress\$4) same (despread\$4 or de adj1 spread\$3) same (DLL or DPLL or delay\$3 adj2 lock\$3 adj2 loop\$1 or phase adj2 lock\$3 adj2 loop\$1)	USPA T; US-P GPUB ; EPO; JPO	2004/08/28 19:50	

	Error Definition	Er ro rs
1		0
2		0

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
3	BRS	L19	446	(interference near2 cancel\$8 or interference near2 eliminat\$4 or interference near2 remov\$4 or noise near2 cancel\$8 or noise near3 eliminat\$4 or noise near3 remov\$4 or noise near2 suppress\$4 or interference near2 suppress\$4) same (despread\$4 or de adj1 spread\$3)	USPAT; US-PGPUB; EPO; JPO	2004/08/28 19:50	
4	BRS	L20	59	19 and (DLL or DPLL or delay\$3 adj2 lock\$3 adj2 loop\$1 or phase adj2 lock\$3 adj2 loop\$1)	USPAT; US-PGPUB; EPO; JPO	2004/08/28 19:51	
5	BRS	L31	123	(phase near2 track\$4 or phase near2 control\$4 or track\$4 near2 loop\$1 or sync\$15 near2 track\$4 or track\$4 near2 loop or sync\$15 near2 recover\$3) same (despread\$3 with receiv\$4 or de adj1 spread\$3 with receiv\$4 or despread\$3 with reception or de adj1 spread\$3 with reception) same (spread\$3 with transmit\$4 or spread\$3 with transmission)	USPAT; US-PGPUB; EPO; JPO	2004/08/28 20:18	

	Error Definition	Er ro rs
3		0
4		0
5		0

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
6	BRS	L32	81	(transmit\$4 with receiv\$3 or reception with transmission) same (sync\$15 near3 track\$4 or sync\$15 near3 acquisition or track\$4 near3 acquisition or track\$3 near2 circuit\$3) same (spread\$3) same (despread\$3 or de adj1 spread\$3)	USPA T; US-P GPUB ; EPO; JPO	2004/08/2 8 20:23	
7	BRS	L33	81	(transmit\$4 with receiv\$3 or reception with transmission) same (sync\$18 near3 track\$4 or sync\$18 near3 acquisition or track\$4 near3 acquisition or track\$3 near2 circuit\$3) same (spread\$3) same (despread\$3 or de adj1 spread\$3)	USPA T; US-P GPUB ; EPO; JPO	2004/08/2 8 20:26	
8	BRS	L34	20	(DLL or PLL or DPLL or delay\$3 adj2 lock\$3 adj2 loop\$1 or phase adj2 lock\$3 adj2 loop\$1) and 33	USPA T; US-P GPUB ; EPO; JPO	2004/08/2 8 20:26	

	Error Definition	Er ro rs
6		0
7		0
8		0